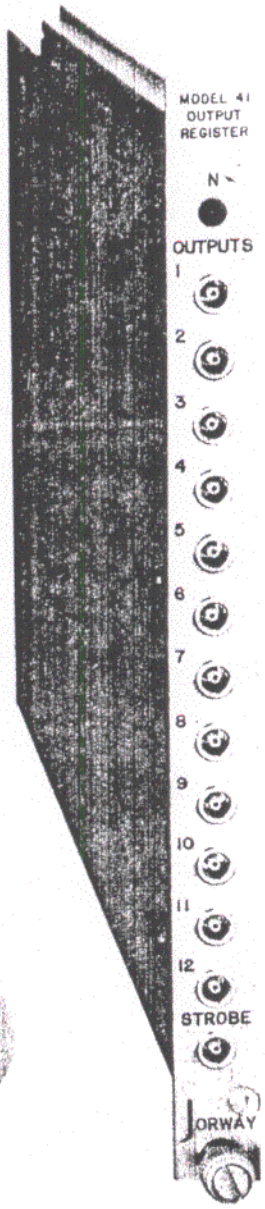
JORWAY

# MODEL 41
# OUTPUT REGISTER




The Model 41 12 bit Output Register is a single width Camac compatable module that meets the requirements of EUR4100e. The Model 41 provides up to 12 bits of information to be extracted from the Dataway Write lines. This information is stored and is available at the front panel as fast N.I.M. level signals, or optionally as power drivers. A versitile system of updating the storage registers is offered to provide flexibility of use. Data can be strobed into storage from the write lines by function command F16 or F17. On command F16, data is strobed in at S1 providing D.C. outputs. When pulsed outputs are required, function F17 is used. In response to F17·S1 data will be strobed into storage; at S2, however, all bits in the register will be reset to zero. Each time the register is updated a strobe output signal is generated. In response to F16·S1 it is a 3 μsec. pulse. The strobe signal generated in response to F17·S1 will last from time S1 to S2, as do the data outputs.

In addition to these modes of operation the register may also be selectively set and reset. In this mode the Dataway Write lines are used as enable lines which select the bits that are to respond to the function requested. Function F20 will set to "1" any bit that has its write input at a logic "1". Function F22 will reset to "0" any bit that has its write input at a logic "1". Both F20 and F22 use strobe S1 to perform the operation.

An N light is provided to visually indicate when the module is addressed.

CAMAC

## MODEL 41 SPECIFICATIONS

OUTPUTS:

DATA and STROBE

Fast N.I.M. outputs, current source
Logic "0" I out = 0
Logic "1" I out =-14ma min.
Max. output voltage -2v

Pulse Width:
In response to F16, D.C. Level output
In response to F17, P.W. is time from S1 to S2.

Option 1. Voltage Outputs, Open Collector
Logic "0" +100v max.
Logic "1" +.5v max.
Max. power out 250 mw

DATAWAY COMMANDS (all fully decoded)

| | |
|---|---|
| N·F16·AO·S1 | Overwrites 12 bit storage register with data from write lines W1-W12, and generates a strobe output signal. |
| N·F17·AO·S1 | Same as N·F16·AO·S1 |
| N·F17·AO·S2 | Resets register and strobe output to zero. |
| N·F20·AO·S1 | Sets to "1" the bits of the storage register that have their Write input at logic "1". |
| N·F22·AO·S1 | Resets to "0" the bits of the storage register that have their Write lines at a logic "1". |

DATAWAY CONTROLS

| | |
|---|---|
| Z·S2 | Resets storage register and strobe output to zero. |
| Q | A "Q" signal is generated in response to commands N·(F16+F17+F20+F22)·AO |

| | |
|---|---|
| INDICATORS | An "N" light is provided to visually indicate the module is addressed. The N signal is integrated so that it will respond to a pulse. |
| POWER REQUIREMENTS | +6v 420 ma nominal<br>-6v 200 ma nominal |
| TEMPERATURE RANGE | 0°C to 60°C |
| SIZE | Single Width Camac Module with protective side shields. Output connectors are Lemo RA 00250 or equivalent. |